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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Complete if Known	
Application Number	10/813,731
Filing Date	03/30/2004
First Named Inventor	Swenson
Art Unit	2616
Examiner Name	JAIN, Raj
Attorney Docket Number	02453.0030.NPUS00

Sheet | 1 | of | 1

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No.[1]	Document Number Number-Kind Code[2] (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
/R.J./		US- 2005/074009	04/07/2005	Kanetake	
		US- 2004/246981	12/09/2004	Zhiqun	
		US- 6,914,905	07/05/2005	Yip	
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/R.J./		WO 03/081857	10/02/2003	Intel Corp		

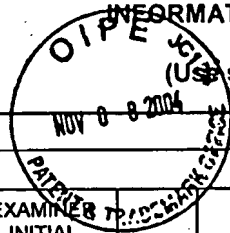| Examiner Signature	/Raj Jain/		Date Considered	07/20/2007

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	ATTY. DOCKET NO. 02453.0030.NPUS00	SERIAL NO. 10/813,731

LIST OF PATENTS AND OTHER ITEMS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

APPLICANT: . Erik R. Swenson et al.	
FILING DATE: March 30, 2004	GROUP: 2661

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EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE
/R.J./	AA	6,295,299	9/25/2001	Haddock, et al.			
/R.J./	AB	6,034,957	3/7/2000	Haddock, et al.			

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		None						X
								X

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/R.J./	AC	Van Ess, D., *A Circular FIFO, PSoC Style*, Cypress Microsystems, Application Note AN2036, Rev. B, pp. 1-5, June 21, 2002.
	AD	Network Working Group, *RFC 1071 – Computing the Internet Checksum*, September 1988, *available at:* http://www.faqs.org/rfcs/rfc1071.html
	AE	Network Working Group, *RFC 1141 – Incremental Updating of the Internet Checksum*, January 1990, *available at:* http://www.faqs.org/rfcs/rfc1141.html
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	AG	Netlogic Microsystems, Product Brief NSE5000GLQ, Copyright 2004, *available at:* http://www.netlogicmicro.com/datasheets/nse5000glq.html
/R.J./	AH	Zhang, Z., *Recovery of Memory and Process in DSM Systems: HA Issue #1*, Hewlett-Packard Co. HPL-2001-76, March 30, 2001.

EXAMINER: /Raj Jain/	DATE CONSIDERED: 07/20/2007

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